

TXB-S422, SMR 04-205 Changes

11 October 2004

Problem

SMR 04-205 attempts stop the Spectra from drifting on a VM-96 system in Texas.

Changes

The baud rate of the Spectra has been changed from 2400 baud to **9600 baud**.
The use of bits 1 and 2 of the DIP switch have been redefined as follows:

Bit #	Old Use	New Use
1	Speed Control	AD CPU Type
2	Speed Control	Speed Control
3	No change	
4	No change	
5	No change	
6	No change	
7	No change	
8	No change	

1. **Bit 1** is now used to select the CPU type.
 - **OFF:** Work the old way with AD-1995, AD-1996, VM-1 and VM-96 type systems.
 - **ON:** Work with the new speed logic for the VM96 type system.
2. **Bit 2** is now used for speed control.
 - **OFF:** Full speed.
 - **ON:** $\frac{1}{2}$ speed.
3. When the TXB receives a “slow” speed it is changed into a stop. Slow is for input speeds are less than or equal to $2^{\circ}/\text{sec}$ in pan or tilt.
4. Automatically recognizing a VM-96 has been deleted. All choices are forced in the software.

¹\$Header: d:/TXB-S422/RCS/00test.tex,v 1.95 2004-07-30 15:23:13-07 Hamilton Exp Hamilton \$

²\$Header: d:/TXB-S422/RCS/SMR04205.inc,v 1.1 2004-10-11 14:53:11-07 Hamilton Exp Hamilton \$