

# TXB-S422, Rev 1.10 Changes

24 September 2003

## Problem

Rev 1.10 of the TXB-S422 translator has been changed in response to data received from the field with a new type of American Dynamics control CPU. The new type is an AD-1024 MegaPower II Central Processing Unit. This unit is installed at a potential customer's site. A team of Pelco engineers examined its outputs and have modified the TXB-S422 to support the new set of speeds that are generated by it.

## Changes

To accomplish the required changes, the use of bits 1 and 2 of the DIP switch have been redefined as follows:

Bit #	Old Use	New Use
1	Speed Control	AD CPU Type
2	Speed Control	Speed Control
3	No change	
4	No change	
5	No change	
6	No change	
7	No change	
8	No change	

1. **Bit 1** is now used to select the CPU type.

- **OFF:** Work the old way with AD-1995, AD-1996, VM-1 and VM-96 type systems.
- **ON:** Work the new way with AD-1024, VM-1 and VM-96 type systems.

2. **Bit 2** is now used for speed control.

- **OFF:** Full speed.
- **ON:**  $\frac{1}{2}$  speed.

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<sup>1</sup>\$Header: d:/TXB-S422/RCS/00test.tex,v 1.91 2003-09-09 08:43:22-07 Hamilton Exp Hamilton \$

<sup>2</sup>\$Header: d:/TXB-S422/RCS/rev110.inc,v 1.2 2003-09-09 08:42:32-07 Hamilton Exp Hamilton \$